

REMARKS

This Application has been carefully reviewed in light of the Official Action dated September 30, 2003. In order to advance prosecution of the present Application, Claims 1-5, 10-12, 16, and 20 have been amended. Applicant respectfully requests reconsideration and favorable action in this Application.

Claims 1-15 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Davis in view of Somasundaram, et al. Independent Claims 1 and 11 recite in general the ability to capture information immediately prior to and immediately subsequent to a triggering event. By contrast, the Davis patent only captures information prior to a triggering event. Once the triggering event occurs, storage of information is stopped and the memory is uploaded for evaluation. The Somasundaram, et al. patent does not include an on-chip trace recorder capable of capturing any information associated with a triggering event. Support for the above recitation can be found at page 8, lines 5-24, of Applicant's specification. Therefore, Applicant respectfully submits that Claims 1-15 are patentably distinct from the proposed Davis - Somasundaram, et al. combination

Claims 16-20 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Davis in view of Somasundaram, et al. and further in view of Microsoft Computer Dictionary. Independent Claim 16 includes a similar limitation discussed above and shown to be patentably distinct from the proposed Davis - Somasundaram, et al. combination. Moreover, the Microsoft Computer Dictionary does not include any additional disclosure combinable with either the Davis or Somasundaram, et al. patents that would be material to patentability of these claims. Therefore, Applicant respectfully submits that

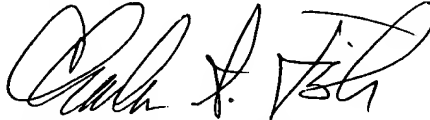
Claims 16-20 are patentably distinct from the proposed Davis - Somasundaram, et al. - Microsoft Computer Dictionary combination.

Applicant has now made an earnest attempt to place the Application in condition for allowance. For the foregoing reasons and for other reasons clearly apparent, Applicant respectfully requests reconsideration and full allowance of Claims 1-20.

The Commissioner is hereby authorized to charge any amount required or credit any overpayment to Deposit Account No. 02-0384 of BAKER BOTTS L.L.P.

Respectfully submitted,  
BAKER BOTTS L.L.P.

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PATENT APPLICATION  
09/788,174



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Kenneth C. Yeager, et al.  
Serial No.: 09/788,174  
Filing Date: February 15, 2001  
Group Art Unit: 2184  
Examiner: Emerson C. Puente  
Title: METHOD AND APPARATUS FOR RECORDING  
TRACE DATA IN A MICROPROCESSOR BASED  
INTEGRATED CIRCUIT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Dear Sir:

CERTIFICATE OF MAILING BY EXPRESS MAIL

I hereby certify that the attached Response to Examiner's Action is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on this 24th day of December 2003, addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Quincy Chambers

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